

Assistant Professor (Tenure-Track) in Semiconductor  
Device Physics, Modeling and Design  
Nanyang Technological University

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Posted Jul. 18, 2024, set to expire May 6, 2025

<b>Job Title</b>	Assistant Professor (Tenure-Track) in Semiconductor Device Physics, Modeling and Design
<b>Department</b>	School of Electrical and Electronic Engineering
<b>Institution</b>	Nanyang Technological University Singapore, Singapore, Singapore
<b>Date Posted</b>	Jul. 18, 2024
<b>Application Deadline</b>	Open until filled
<b>Position Start Date</b>	Available immediately
<b>Job Categories</b>	Assistant Professor
<b>Academic Field(s)</b>	Physics - General
<b>Apply Online Here</b>	<a href="https://ntu.wd3.myworkdayjobs.com/Careers/job/NTU-Main-Campus-Singapore/Assistant-Professor--Tenure-track--in-Semiconductor-Device-Physics--Modeling-and-Design_R00017802">https://ntu.wd3.myworkdayjobs.com/Careers/job/NTU-Main-Campus-Singapore/Assistant-Professor--Tenure-track--in-Semiconductor-Device-Physics--Modeling-and-Design_R00017802</a>

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**Job Description**

Nanyang Technological University (NTU) invites applications for the position of Assistant Professor (Tenure-track) in “Semiconductor Device Physics, Modeling and Design”.

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This position will be situated within the School of Electrical and Electronic Engineering (EEE). Founded in 1981, EEE stands as one of the pioneering schools at NTU. Rooted in a culture of excellence, EEE is celebrated for its rigorous academic standards and impactful research. To bolster advanced research endeavours, EEE is home to 13 research centres and state-of-the-art facilities, outfitted with cutting-edge equipment. In addition to these 13 centres, the school boasts over 50 laboratories, fostering both teaching and research initiatives within its dynamic academic community.

The successful applicant will be responsible for conducting cutting-edge research in semiconductor device physics, encompassing various areas, such as multi-physics-based compact models for advanced semiconductor devices, CAD tool development, mixed-mode circuit simulation, Monte Carlo simulation, ultrafast phenomena, process design kits, model validation, circuit and system simulation, and device characterizations. In addition to research, the successful candidate will actively participate in teaching and administrative duties at a level that aligns with the expectations of a world-class, research-intensive university.

### **Responsibilities**

- Secure research funding to establish and expand research initiatives, while delivering high-quality research outcomes in the form of publications, invention disclosures, engineering designs, and other relevant outputs in the fields of semiconductor device physics, as well as other disciplines associated with Electrical and Electronic Engineering.
- Contribute to the development and implementation of undergraduate and postgraduate curricula in Electrical and Electronic Engineering and related fields.
- Provide support, supervision, and mentorship to undergraduate students involved in project work and other academic endeavors, as well as postgraduate research students in their research projects and career development.
- Actively participate in the School of Electrical and Electronic Engineering, College of Engineering, and University communities by engaging in activities related to research, teaching, enterprise, student experience, and widening participation.

### **Requirements**

Candidates with the following qualifications are encouraged to apply:

- A Ph.D. in Electrical and Electronic Engineering, Physics, or a relevant field
- At least four years of research experience in semiconductor device physics

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- Excellent track record in publishing high-impact papers in top journals and conferences
- Proven track records and expertise in semiconductor device physics
- Preferable to have secured some research funding as principal investigator
- Preferable to have proven track records of teaching and mentoring
- Good interpersonal skills
- A mindset of continual growth and academic excellence

### **Application**

Applicants are invited to submit their applications (cover letter, CV, research and teaching statements, and citation report, where applicable) through the NTU career portal.

Review of applications and shortlisting decision will be on an ongoing basis until the position is filled. Only shortlisted candidates will be notified.

### **Contact Information**

Please reference Academickeys in your cover letter when applying for or inquiring about this job announcement.

#### **Contact**

, Singapore

Singapore

**Contact E-mail**    EEE-Fac-Recruit@ntu.edu.sg